

DEPARTMENT OF AIML. GNDEC BIDAR

MES (21CS43) IMPORTANT QUESTIONS

Module-1

1. Explain the major design rules to implement RISC philosophy, State differences between RISC And CISC processors.
2. Discuss the ARM design philosophy, also discuss about the ARM bus technology
3. Explain ARM core data flow model with a neat diagram
4. Along with neat diagram of an ARM based embedded device (Microcontroller), explain the four main hardware components, Briefly Explain ARM register used under various modes
5. Explain Pipeline in detail.
6. Briefly describe the concept of exceptions, interrupts and the vector table
7. Describe conditional execution. Write the different code suffix.
8. Compare and Contrast microprocessor and microcontroller. 4M
9. Explain ARM core data flow model with a neat diagram. 8M
10. Along with neat diagram of an ARM based embedded device (Microcontroller), explain the four main hardware components. 8M
11. Explain the different processor modes provided by ARM7. 8M Give the schematic of a Current Program Status Register of ARM7 processor briefing the individual bits. 6M
12. What is Pipelining. Explain in detail schematically. 6M
13. Write an ALP to multiply two 16 bit binary numbers.
14. Write an ALP to find the sum of first 10 integer numbers.
15. Write an ALP to find factorial of a number.
16. Write an ALP to add an array of 16 bit numbers and store the 32 bit result in internal RAM.

Module-2

1. What do you mean by arithmetic instructions in ARM processor, Discuss the load & store Instructions with respect to the Single Register Transfer
2. With a neat diagram explain Barrel Shifter
3. Explain the MOV instruction set provided by ARM7 with the example for each, briefly Explain the working of ARM Swap instruction
4. Define instruction scheduling? Explain the rules summarizing the cycle timings for common instruction classes on the ARM9TDMI.
5. Explain the scheduling of following instructions i) STR ii) LDRH iii) B Label - 12M
6. Write a note on Profiling and Cycle Counting.
7. Explain the ARM Single-Register and Multiple-Register load-store addressing modes with examples.
8. Explain Co-Processor instructions of ARM Processor.
9. Explain the MOV instruction set provided by ARM7 with the example for each. 8M
10. Explain the ARM swap instruction with an example code. 6M
11. Brief about the categories of Load-Store instructions used with ARM. 6M
12. Write an ALP to find the square of a number (1 to 10) using look-up table.
13. Write an ALP to find the largest/smallest number in an array of 32 numbers.
14. Write an ALP to arrange a series of 32 bit numbers in ascending/descending order.
15. Write an ALP to count the number of ones and zeros in two consecutive memory locations.

Module-3

1. Explain the system core of the Embedded systems, Explain briefly the applications and purposes of Embedded Systems
2. Explain the history of ES, Also differentiate between General Purpose Computing Systems and Embedded Systems.
3. What are the different types of memories used in Embedded System design? Explain the role of each
4. Explain the following i) 7-Segment LED Display ii) Stepper Motor. iii) PLD and its types iv) OCI in ES v) Oscillator unit in ES (20M/4M each)
5. Explain the following: i) I2C ii) 1-Wire Interface iii) SPI Interface iv) Reset Circuit - 12M.
6. What are the different types of memories used in Embedded System design?
7. Explain the role of each. 10M
8. List different purposes of embedded system with examples. 10M
9. Briefly Describe the classification of embedded systems 8M
10. Explain the following: i. I2C ii. 1-Wire Interface iii. SPI Interface iv. Reset Circuit

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Module-4

1. Explain briefly the characteristics, operational and non-operational quality attributes of Embedded systems.
2. Explain Quality attribute in embedded system development ?What are the different quality attribute to be considered in an embedded system design
3. With the functional block diagram(if required), explain the operation of (i) Washing Machine as Application-Specific Embedded system (ii) Tea/Coffee vending machine (iii)Automatic seat belt warning system
4. Explain the basic approaches for designing Embedded Firmware.
5. Explain time to market and time to prototype. Explain its significance in product development.
6. Explain with a neat block diagram, how source file to object file translation takes place.
7. Explain the fundamental issues in hardware software co-design.
8. What are the operational and non-operational quality attributes of an embedded systems. 10M
9. Explain the different types of serial interface bus used in Automotive Communication. 4M
10. Design FSM model for tea/coffee vending machine. 6M
11. Explain the fundamental issues in hardware software co-design. 6M
12. Explain with a neat block diagram, how source file to object file translation takes Place. 8M
13. Explain the different embedded firmware design approaches. 6M

Module-5

1. Define the terms Task, Process and Threads? Explain the Process structure, process states and State transitions.
2. Explain the functional and non-functional requirements to be considered while choosing an RTOS for an Embedded design
3. Explain Multi-Threading, Write a multithreaded application to print "Hello I'm in main thread" "From main thread and "Hello I'm in new thread"
4. Explain the role of Integrated Development Environment (IDE) for embedded software Development.
5. With neat diagram explain operating system architecture
6. Differentiate between hard real time and soft real time operating system with an example for each. 4M
7. With neat diagram explain operating system architecture. 8M
8. Explain the Simulator and Emulator. 8M
9. Write a note on message passing. 8M
10. Explain the concept of deadlock with a neat diagram